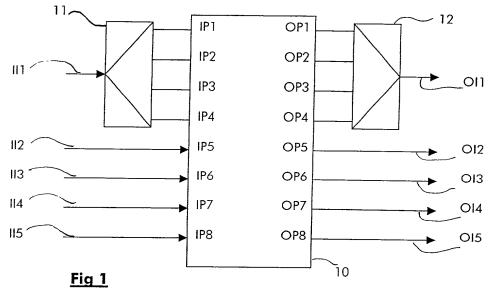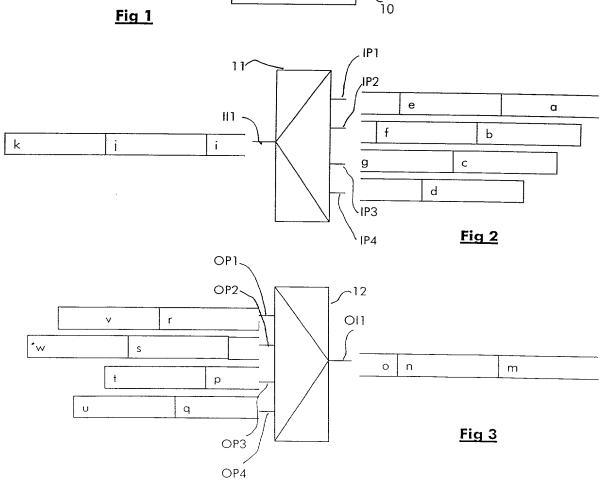"A data packet switching node accommodating
very high bit rate interfaces"
Albert LESPAGNOL
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Fig 1

Fig 2

Fig 3

"A data packet switching node accommodating
very high bit rate interfaces"
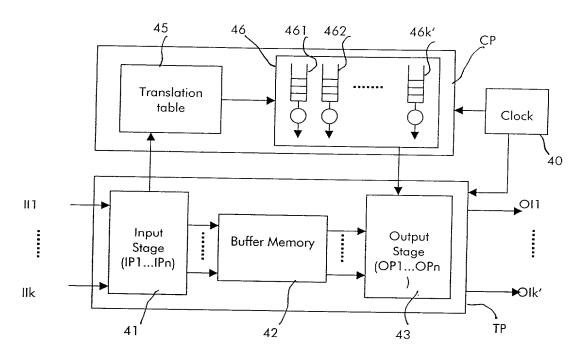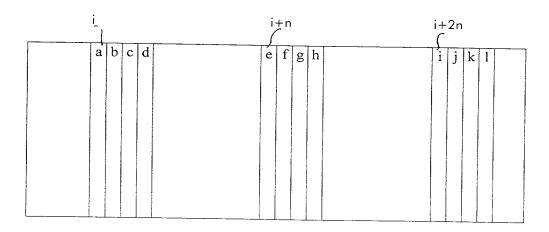Albert LESPAGNOL
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Fig 4

Fig 5